

PATENT APPLICATION  
ATTORNEY DOCKET NO. 5038-338

IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Pete D. Vogt

Serial No.: 10/713,868

Examiner: unknown

Filing Date: November 14, 2003

Group Art Unit: 2182

Title: DATA ACCUMULATION BETWEEN DATA PATH AND MEMORY DEVICE

Mail Stop Amendments  
Commissioner for Patents  
P.O. Box 1450  
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**INFORMATION DISCLOSURE STATEMENT**

This Information Disclosure Statement is submitted:

under 37 CFR 1.97 (b); or  
(Within three months of filing national application; or date of entry of International application; or before mailing date of first office action on the merits; whichever occurs last)

under 37 CFR 1.97 (c) together with either a:  
 Statement under 37 CFR 1.97 (e), or  
 a \$180 fee under 37 CFR 1.17 (p); or

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Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

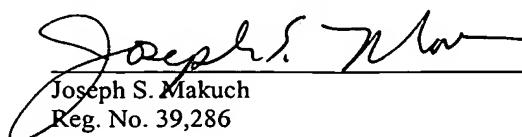
It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A duplicate copy of this sheet is enclosed.

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Respectfully submitted,

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Joseph S. Makuch  
Reg. No. 39,286

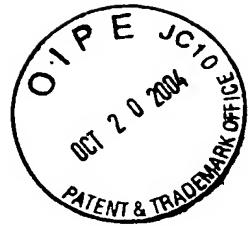
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**INFORMATION DISCLOSURE CITATION  
FORM PTO-1449 (Modified)**

**U.S. PATENT DOCUMENTS**

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub</u> <u>Class</u>

**FOREIGN PATENT DOCUMENTS**

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>

**OTHER DOCUMENTS**

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
		The Institute of Electrical and Electronics Engineers, Inc., <i>IEEE Standard for Scalable Coherent Interface (SCI)</i> , May 23, 2001, 1-243 pp.
		R. Ng (Sun Microsystems, Inc.), <i>Fast Computer Memories</i> , IEEE Spectrum, October 1992, pp. 36-39
		R.H.W. Salters (Philips Research Laboratories), <i>Fast DRAMs for Sharper TV</i> , IEEE Spectrum, October 1992, pp. 40-42
		F. Jones (United Memories, Inc.), <i>A New Era of Fast Dynamic RAMs</i> , IEEE Spectrum, October 1992, pp. 43-49
		M. Farmwald and D. Mooring (Rambus, Inc.), <i>A Fast Path to One Memory</i> , IEEE Spectrum, October 1992, pp. 50-51
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		The Institute of Electrical and Electronics Engineers, Inc., <i>IEEE Standard for High-Bandwidth Memory Interface Based on Scalable Coherent Interface (SCI) Signaling Technology (RamLink)</i> , 1996, 1-91 pp.

Examiner: \_\_\_\_\_  
Date Considered: \_\_\_\_\_